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APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\* *0 AM*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*  
 JAPAN 2003-115248 04/21/2003 *YES AM*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
 \*\* 06/27/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 3	TOTAL CLAIMS 8	INDEPENDENT CLAIMS 4
Verified and Acknowledged	<i>AM</i> Examiner's Signature Initials				

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 27572  
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TITLE  
 Clock signal detection circuit and semiconductor integrated circuit using the same

FILING FEE  RECEIVED 986	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____
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